UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,081,607 B1
APPLICATION NO. : 09/678025
DATED : July 25, 2006
INVENTOR(S) : Toru Koizumi

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

TITLE PAGE ITEM (54)

 Title, "SOLID STATE" should read--SOLID-STATE--.

COLUMN 1

 Line 1, "SOLID STATE" should read --SOLID-STATE--.

COLUMN 4

 Line 2, "theen th" should read --the n-th--.

COLUMN 7

 Line 35, "pulse VR" should read --pulse ΦVR;
 Line 40, "VR." should read --ΦVR.--;
 Line 41, "pulse S1" should read --pulse ΦS1--; and
 Line 44, "pulse 22" should read --pulse Φ22--.

COLUMN 10

 Line 60, "denote" should read --denotes--.

COLUMN 12
 Line 51, "Other one" should read --another--.

COLUMN 15

 Line 14, "sectional" should read --sectional view--;
 Line 25, "MOS" should read --MOS transistors--;
 Line 44, "necessity," should read --necessary,--; and
 Line 52, "signal" should read --signals--.

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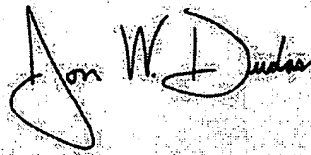DATED   : July 25, 2006
INVENTOR(S)   : Toru Koizumi

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 16

Line 64, "means" should read --means;--.

Signed and Sealed this

Thirtieth Day of January, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office